



**MOTOROLA**

Chip Errata  
**DSP56001 Digital Signal Processor**  
Mask: B77G

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**ERRATA**

<u>Errata Description</u>	<u>Applies to Mask</u>
1. Fast interrupt service routines consisting of a single two-word instruction that returns to a conditional JSR (condition true) cause both the loop and the trace flags to be cleared and corrupt the interrupt mask bits (I1, I0) in the status register (SR) that is stacked. This condition may have a low probability of occurrence in the user's program, but for complete software reliability, it is strongly recommended that the user restrict fast interrupt routines to only two single word instructions.	B77G
2. The DSP will stop fetching instructions and will not resume operation until power is cycled if an illegal instruction (i. e. an unrecognized op code) is executed.	B38E B39P B77G

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**NOTES**

1. An over-bar (i.e.  $\overline{\text{xxxx}}$ ) indicates an active-low signal.
2. The letters seen to the right of the errata tell which DSP56002 mask numbers apply.

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